

GAU 2751  
\$ # 7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Mailloux, et al.

Serial No.: 08650,719

Filed: May 20, 1996

For: BURST/PIPELINED EDO  
MEMORY DEVICE

§  
§ Group Art Unit: 2751  
§  
§ Examiner: Kim, H.  
§  
§ Atty. Docket: 95-0653.00  
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RECEIVED  
MAR 02 1999  
Group 2700

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Certificate of Mailing (37 C.F.R. § 1.8)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:

2/19/99 : *Peggy Kaye Foster*  
Date Signature

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant[s] respectfully request[s] that this Information Disclosure Statement be entered and that the references listed on the attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. § 1.97(b), this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possible material information as defined in 37 C.F.R. § 1.56(a) exists.

The following references are submitted for the Examiner's review:

U.S. Patents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
5,652,724	7/97	Manning
5,457,659	10/95	Schaefer
5,452,261	9/95	Chung et al.

5,682,354	10/97	Manning
5,640,364	06/97	Merritt et al.
5,729,504	12/95	Cowles
5,661,695	08/97	Zagar et al.
5,305,284	04/94	Iwase
5,325,330	06/94	Morgan
5,325,502	06/94	McLaury
5,373,227	12/94	Keeth
5,410,670	04/95	Hansen et al.
5,349,566	09/94	Merritt et al.
5,668,773	09/97	Zagar et al.
4,870,622	09/89	Aria et al.
5,058,066	10/91	Yu
5,280,594	01/94	Young et al.

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Other References

Micron Technology, Inc. , "1995 DRAM Data Book" pp. 4-1 thru 4-42, 12/95.

"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories, entire book.

Related Co-pending Applications

PCT Patent Application No. PCT/US95/16656, filed 12/21/1995, entitled: Burst Edo Memory Device

Address Counter.

PCT Patent Application No. PCT/US95/16984 , filed 12/22/1995, entitled: Burst Edo Memory Device.

This Information disclosure Statement is being submitted after the mailing of the first Office Action, but before the mailing of a Final Rejection or Notice of Allowance. The Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) of \$240.00 and any additional fees which may be required to Micron Technology, Inc. Deposit Account No. 13-3092, Order No. 95-0653.00.

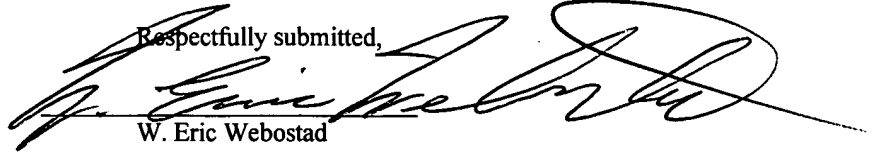
If there are any matters which may be resolved or clarified through telephone interview, the Examiner is respectfully requested to contact Applicant's undersigned attorney at the number indicated.

\* \* \* \*

A Form PTO-1449 is enclosed herewith.

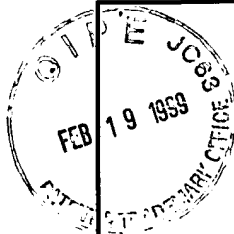
Date: 2-18-99

Respectfully submitted,



W. Eric Webostad  
Reg. No. 35,406  
Micron Technology, Inc.  
8000 S. Federal Way  
Boise, ID 83706-9632  
(208) 368-4792

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



## FEE TRANSMITTAL

## Complete if Known

Application Number		08/650,719
Filing Date		May 20, 1996
First Named Inventor		Mailloux, et al.
Group Art Unit		2751
Examiner Name		Kim, H.
Attorney Docket Number		95-0653
TOTAL AMOUNT OF PAYMENT		(\$ ) 240

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MAR 02 1999

Group 2700

METHOD OF PAYMENT (check one)		FEE CALCULATION (continued)																																																																																																																																																																															
1. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge indicated fees and credit any over payments to: Deposit Account Number: <b>13-3092 /95-0653</b> Deposit Account Name: <b>MICRON TECHNOLOGY, INC.</b> <input checked="" type="checkbox"/> Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17 <input type="checkbox"/> Charge the Issue Fee Set in 37 CFR 1.18 at the Mailing of the Notice of Allowance, 37 CFR 1.31(b)		3. ADDITIONAL FEES <table border="1"> <thead> <tr> <th>Large Fee Code</th> <th>Entity Fee (\$)</th> <th>Small Fee Code</th> <th>Entity Fee (\$)</th> <th>Fee Description</th> <th>Fee Paid</th> </tr> </thead> <tbody> <tr><td>105</td><td>130</td><td>205</td><td>65</td><td>Surcharge - late filing fee or oath</td><td>_____</td></tr> <tr><td>127</td><td>50</td><td>227</td><td>25</td><td>Surcharge - late provisional filing or cover sheet.</td><td>_____</td></tr> <tr><td>139</td><td>130</td><td>139</td><td>130</td><td>Non-English specification</td><td>_____</td></tr> <tr><td>147</td><td>2,520</td><td>147</td><td>2,520</td><td>For filing a request for reexamination</td><td>_____</td></tr> <tr><td>112</td><td>920*</td><td>112</td><td>920*</td><td>Requesting publication of SIR prior to Examiner action</td><td>_____</td></tr> <tr><td>113</td><td>1,840*</td><td>113</td><td>1,840*</td><td>Requesting publication of SIR after Examiner action</td><td>_____</td></tr> <tr><td>115</td><td>110</td><td>215</td><td>55</td><td>Extension for response within first month</td><td>_____</td></tr> <tr><td>116</td><td>400</td><td>216</td><td>200</td><td>Extension for response within second month</td><td>_____</td></tr> <tr><td>117</td><td>950</td><td>217</td><td>475</td><td>Extension for response within third month</td><td>_____</td></tr> <tr><td>118</td><td>1,570</td><td>218</td><td>755</td><td>Extension for response within fourth month</td><td>_____</td></tr> <tr><td>119</td><td>310</td><td>219</td><td>155</td><td>Notice of Appeal</td><td>_____</td></tr> <tr><td>120</td><td>310</td><td>220</td><td>155</td><td>Filing a brief in support of an appeal</td><td>_____</td></tr> <tr><td>121</td><td>270</td><td>221</td><td>135</td><td>Request for oral hearing</td><td>_____</td></tr> <tr><td>138</td><td>1,510</td><td>138</td><td>1,510</td><td>Petition to institute a public use proceeding</td><td>_____</td></tr> <tr><td>140</td><td>110</td><td>240</td><td>55</td><td>Petition to revive unavoidably abandoned application</td><td>_____</td></tr> <tr><td>141</td><td>1,320</td><td>241</td><td>660</td><td>Petition to revive unintentionally abandoned application</td><td>_____</td></tr> <tr><td>142</td><td>1,320</td><td>242</td><td>660</td><td>Utility issue fee (or reissue)</td><td>_____</td></tr> <tr><td>143</td><td>450</td><td>243</td><td>225</td><td>Design issue fee</td><td>_____</td></tr> <tr><td>144</td><td>670</td><td>244</td><td>335</td><td>Plant issue fee</td><td>_____</td></tr> <tr><td>122</td><td>130</td><td>122</td><td>130</td><td>Petitions to the Commissioner</td><td>_____</td></tr> <tr><td>123</td><td>50</td><td>123</td><td>50</td><td>Petitions related to provisional applications</td><td>_____</td></tr> <tr><td>126</td><td>240</td><td>126</td><td>240</td><td>Submission of Information Disclosure Stmt</td><td>240</td></tr> <tr><td>581</td><td>40</td><td>581</td><td>40</td><td>Recording each patent assignment per property (times number of properties)</td><td>_____</td></tr> <tr><td>146</td><td>790</td><td>246</td><td>395</td><td>Filing a submission after final rejection (37 CFR 1.129(a))</td><td>_____</td></tr> <tr><td>149</td><td>790</td><td>249</td><td>395</td><td>For each additional invention to be examined (37 CFR 1.129(b))</td><td>_____</td></tr> <tr><td colspan="5">Other fee (specify) _____</td><td>_____</td></tr> <tr><td colspan="5">Other fee (specify) _____</td><td>_____</td></tr> <tr><td colspan="5">SUBTOTAL (3)</td><td>(\$ ) 240</td></tr> </tbody> </table>		Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid	105	130	205	65	Surcharge - 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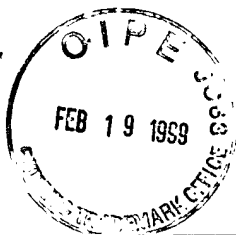
02/26/1999 SLUANG 00000044 133092

01 FC:126

Total Claims 16 - 18 = 0 X 22 = 0  
 Independent Claims = 0 X 82 = 0  
 Multiple Dependent Claims = X =

Large Entity Fee Code (\$)  
 Small Entity Fee Code (\$)  
 Fee Description  
 103 22 203 11 Claims in excess of 20  
 102 82 202 41 Independent claims in excess of 3  
 104 270 204 135 Multiple dependent claim  
 109 82 209 41 Reissue independent claims over original patent  
 110 22 210 11 Reissue claims in excess of 20 and over original patent

SUBMITTED BY		Complete (if applicable)	
Typed or Printed Name	W. Eric Wobstad	Reg. Number	35,406
Signature		Date	2-19-99
		Deposit Acct. User ID	13-3092/95-0653



Sheet: 1 of: 2

FORM: PTO-1449 (REV: 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Atty Docket No: <b>95-0653</b>	Serial No: <b>08/650,719</b>
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (37 CFR 1.98(b)) (use several sheets if necessary)		Applicant: <b>Mailloux et al.</b>	
		Filing Date: <b>5/20/96</b>	Group: <b>2751</b>

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	
	AA 5,652,724	7/97	Manning	365	189.05	
	AB 5,457,659	10/95	Schaefer	365	222	
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	AK 5,349,566	09/94	Merritt et al.	365	233.5	

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
	AL					<input type="checkbox"/>	<input type="checkbox"/>
	AM					<input type="checkbox"/>	<input type="checkbox"/>
	AN					<input type="checkbox"/>	<input type="checkbox"/>
	AO					<input type="checkbox"/>	<input type="checkbox"/>
	AP					<input type="checkbox"/>	<input type="checkbox"/>
	AQ					<input type="checkbox"/>	<input type="checkbox"/>

Initial

OTHER ART (including author, title, date, pertinent pages, etc.)

	AR		Micron Technology, Inc., "1995 DRAM Data Book" pp. 4-1 thru 4-42, 12/95
			"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories, entire book.
			Date
	AT		PCT Patent Application No. PCT/US95/16984, filed 12/22/1995, entitled: Burst Edo Memory Device.

Examiner:	Date Considered:
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.

FORM: PTO-1449 (REV: 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Atty Docket No: <b>95-0653</b>	Serial No: <b>08/650,719</b>
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## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	
	AA	5,682,354	10/97	Manning	365	233.5
	AB	5,640,364	06/97	Merritt et al.	365	233.5
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	AO					<input type="checkbox"/>	<input type="checkbox"/>
	AP					<input type="checkbox"/>	<input type="checkbox"/>
	AQ					<input type="checkbox"/>	<input type="checkbox"/>

Initial

OTHER ART (including author, title, date, pertinent pages, etc.)

			PCT Patent Application No. PCT/US95/16656, filed 12/21/1995, entitled: Burst Edo Memory Device Address Counter.
	AS		
	AT		

Examiner:	Date Considered:
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.